PCN Number:	20191112001.1	**PCN Date:**	Nov 18, 2019
Title:	Qualification of RFAB as an additional Fab site option for select devices and Datasheet Update		
Customer Contact:	PCN Manager	**Dept:**	Quality Services
Proposed 1st Ship Date:	Feb 18, 2020	**Estimated Sample Availability:**	Date provided at sample request.

Change Type:

☐	Assembly Site	☐	Assembly Process	☐	Assembly Materials
☐	Design	☒	Electrical Specification	☐	Mechanical Specification
☐	Test Site	☐	Packing/Shipping/Labeling	☐	Test Process
☐	Wafer Bump Site	☐	Wafer Bump Material	☐	Wafer Bump Process
☒	Wafer Fab Site	☒	Wafer Fab Materials	☐	Wafer Fab Process
		☐	Part number change		

Notification Details

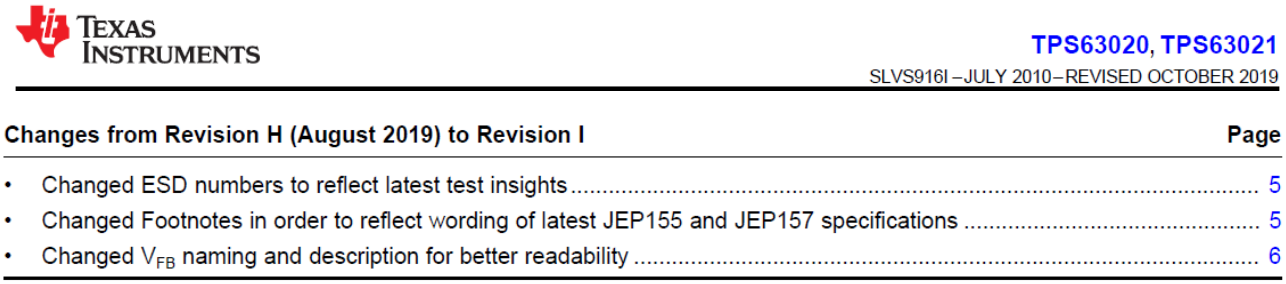
Description of Change:

Texas Instruments is pleased to announce the qualification of its RFAB fabrication facility as an additional Wafer Fab source for the selected devices listed in the "Product Affected" section.

Current Fab Site			Additional Fab Site		
Current Fab Site	**Process**	**Wafer Diameter**	**New Fab Site**	**Process**	**Wafer Diameter**
FFAB	LBC7	200 mm	RFAB	LBC7	300 mm

As part of the RFAB qualification, it was determined that the previous Datasheet ESD limits were not accurate (this is also true for FFAB). This has been corrected and the datasheet number will be changing as shown below:

Device Family	Change From:	Change To:
TPS63020, TPS63021	SLVS916H	SLVS916I



TEXAS INSTRUMENTS

TPS63020, TPS63021

SLVS916I – JULY 2010 – REVISED OCTOBER 2019

Changes from Revision H (August 2019) to Revision I **Page**

- Changed ESD numbers to reflect latest test insights 5
- Changed Footnotes in order to reflect wording of latest JEP155 and JEP157 specifications 5
- Changed V_{FB} naming and description for better readability 6

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/product/TPS63020

Reason for Change:

Continuity of supply and to accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

None.

Changes to product identification resulting from this PCN:

Fab Site Information:

Chip Site	Chip Site Origin Code (20L)	Chip Site Country Code (21L)	Chip Site City
FR-BIP-1	TID	DEU	Freising
RFAB	**RFB**	**USA**	**Richardson**

Sample product shipping label (not actual product label)

Product Affected:

Group 1: Adding RFAB as an additional site		
TLV62080DSGR	TLV62084DSGR	TPS62080DSGR
TLV62080DSGT	TLV62084DSGT	TPS62080DSGT

Group 2: Adding RFAB and Datasheet update			
TPS63020DSJR	TPS63020DSJT	TPS63021DSJR	TPS63021DSJT

Qualification Report

Approve Date 24-August-2019

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	Test Name / Condition	Duration	Qual Device: TPS63020DSJR	QBS Process Reference: TPS2543QRTE	QBS Package Reference: SN1010017RSAR2	QBS Package Reference: TPS2546RTER
AC	Autoclave, 2 atm, 121C	96 Hours	-	3/231/0	3/231/0	-
ED	Electrical Characterization	Per Datasheet Parameters	Pass	Pass	Pass	-
ELFR	Early Life Failure Rate, 125C	48 Hours	-	3/2400/0	-	-
HAST	Biased HAST, 130C/85%RH	96 Hours	-	3/231/0	-	3/231/0
HBM	ESD – HBM exclude pins VIN, VINA, L1	2000 V	1/3/0	-	-	-
HBM	ESD – HBM all pins	500 V	1/3/0	-	-	-
CDM	ESD - CDM	1500 V	1/3/0	-	-	-
HTOL	Life Test, 150C	300 Hours	-	3/231/0	3/231/0	-
HTSL	High Temp. Storage Bake, 170C	420 Hours	-	-	3/231/0	1/77/0
HTSL	High Temp. Storage Bake, 175C	500 Hours	-	3/135/0	-	-
LU	Latch-up	(per JESD78)	1/6/0	1/6/0	-	-
TC	Temperature Cycle, -55/150C	700 Cycles	-	-	3/231/0	-
TC	Temperature Cycle, -65/150C	500 Cycles	-	3/231/0	-	1/77/0

- QBS: Qual By Similarity
- Qual Device TPS63020DSJR is qualified at LEVEL1-260C

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

Green/Pb-free Status:

Qualified Pb-Free (SMT) and Green

Qualification Report

Approve Date 25-September-2019

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	Test Name / Condition	Duration	Qual Device: TLV62080DSGR	QBS Process Reference: TPS2543QRTE	QBS Package Reference: TPS61021DSG	QBS Package Reference: TPS62170DSG
AC	Autoclave, 2 atm, 121C	96 Hours	-	3/231/0	-	-
ED	Electrical Characterization	Per Datasheet Parameters	Pass	Pass	Pass	Pass
ELFR	Early Life Failure Rate, 125C	48 Hours	-	3/2400/0	-	
HAST	Biased HAST, 130C/85%RH	96 Hours	-	-	-	3/231/0
HAST	Biased HAST, 110C/85%RH	264 Hours	-	3/231/0	3/231/0	-
HBM	ESD - HBM	2000 V	1/3/0	1/3/0	-	-
CDM	ESD - CDM	500 V	1/3/0	1/3/0	-	-
HTOL	Life Test, 150C	300 Hours	-	3/231/0	-	-
HTSL	High Temp. Storage Bake, 170C	420 Hours	-	-	2/90/0	2/90/0
LU	Latch-up	(per JESD78)	1/6/0	1/6/0	-	-
TC	Temperature Cycle, -65/150C	500 Cycles	-	3/231/0	3/231/0	-
UHAST	Unbiased HAST, 130C/85%RH	96 Hours	-	-	3/231/0	3/231/0

- QBS: Qual By Similarity
- Qual Device TLV62080DSGR is qualified at LEVEL2-260C

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

Green/Pb-free Status:
Qualified Pb-Free(SMT) and Green

Qualification Report

27-September-2019

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	Test Name / Condition	Duration	Qual Device: TLV62084DSGR	QBS Process Reference: TPS2543QRTE	QBS Package Reference: TPS61021DSG	QBS Package Reference: TPS62170DSG
AC	Autoclave, 2 atm, 121C	96 Hours	-	3/231/0	-	-
ED	Electrical Characterization	Per Datasheet Parameters	Pass	Pass	Pass	Pass
ELFR	Early Life Failure Rate, 125C	48 Hours	-	3/2400/0	-	
HAST	Biased HAST, 130C/85%RH	96 Hours	-	-	-	3/231/0
HAST	Biased HAST, 110C/85%RH	264 Hours	-	3/231/0	3/231/0	-
HBM	ESD - HBM	2000 V	1/3/0	1/3/0	-	-
CDM	ESD - CDM	500 V	1/3/0	1/3/0	-	-
HTOL	Life Test, 150C	300 Hours	-	3/231/0	-	-
HTSL	High Temp. Storage Bake, 170C	420 Hours	-	-	2/90/0	2/90/0
LU	Latch-up	(per JESD78)	1/6/0	1/6/0	-	-
TC	Temperature Cycle, -65/150C	500 Cycles	-	3/231/0	3/231/0	-
UHAST	Unbiased HAST, 130C/85%RH	96 Hours	-	-	3/231/0	3/231/0

- QBS: Qual By Similarity
- Qual Device TLV62084DSGR is qualified at LEVEL2-260C

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

Green/Pb-free Status:
Qualified Pb-Free (SMT) and Green

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