

Vishay Siliconix

Dual P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
- 20	0.015 at V _{GS} = - 4.5 V	- 9.4		
	0.019 at V _{GS} = - 2.5 V	- 8.4		
	0.024 at V _{GS} = - 1.8 V	- 7.5		

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

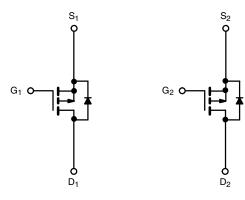
APPLICATIONS

Load Switching



Available

SO-8 S_1 D_1 8 G1 D₁ 2 7 D_2 S_2 6 3 G₂ 5 D_2 Δ Top View



Ordering Information: Si4913DY-T1-E3 (Lead (Pb)-free) Si4913DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$T_A = 25 \ ^{\circ}C$, unless	ss otherwise r	noted			
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 20		V	
Gate-Source Voltage		V _{GS}	± 8		v	
	T _A = 25 °C	- I _D	- 9.4	- 7.1		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 7.5	- 5.7	٨	
Pulsed Drain Current		I _{DM}	- 30		A	
Continuous Source Current (Diode Conduction) ^a		۱ _S	- 1.7	- 0.9		
Maximum Power Dissipation ^a	T _A = 25 °C	D	2.0	1.1	W	
	T _A = 70 °C	P _D	1.3	0.7	vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	- R _{thJA}	45	62.5	°C/W
Maximum Junction-to-Ambient ^a	Steady State		85	110	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	26	35	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	t Conditions Min. Typ.		Max.	Unit	
Static	•		•				
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -500 \ \mu A$ - 0.40 - 1.		- 1.0	V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -20 V, V_{GS} = 0 V$	- 1		μΑ		
		V_{DS} = - 20 V, V_{GS} = 0 V, T_{J} = 55 °C	- 5				
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 30			А	
Drain-Source On-State Resistance ^a		V _{GS} = - 4.5 V, I _D = - 9.4 A		0.0125	0.015		
	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 8.4 A		0.0155	0.019	Ω	
		V _{GS} = - 1.8 V, I _D = - 3.0 A		0.020	0.024		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 9.4 A		40		S	
Diode Forward Voltage ^a	V _{SD}	I _S = - 1.7 A, V _{GS} = 0 V		- 0.7	- 1.2	V	
Dynamic ^b							
Total Gate Charge	Qg			43	65		
Gate-Source Charge	Q _{gs}	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -9.4 \text{ A}$		7.1		nC	
Gate-Drain Charge	Q _{gd}			10.9			
Turn-On Delay Time	t _{d(on)}			32	50		
Rise Time	t _r	V_{DD} = 10 V, R_L = 10 Ω		42	65		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 1 A, V_GEN = - 4.5 V, R_g = 6 Ω		350	525	ns	
Fall Time	t _f			160	240	1	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.7 A, dl/dt = 100 A/μs		127	200		

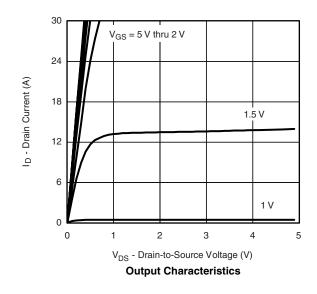
Notes:

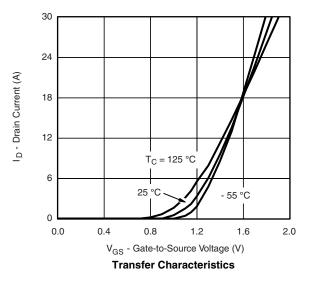
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







Si4913DY

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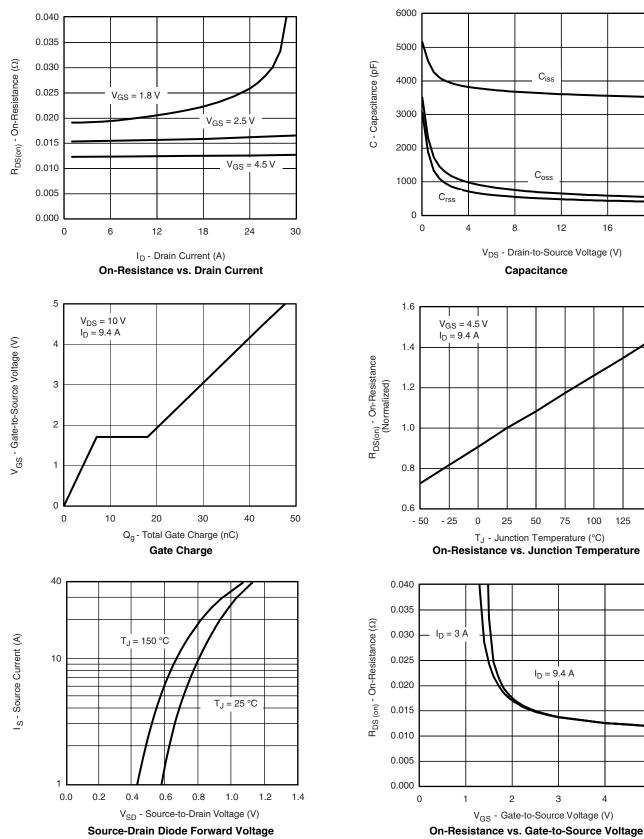
100

125

150

20

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



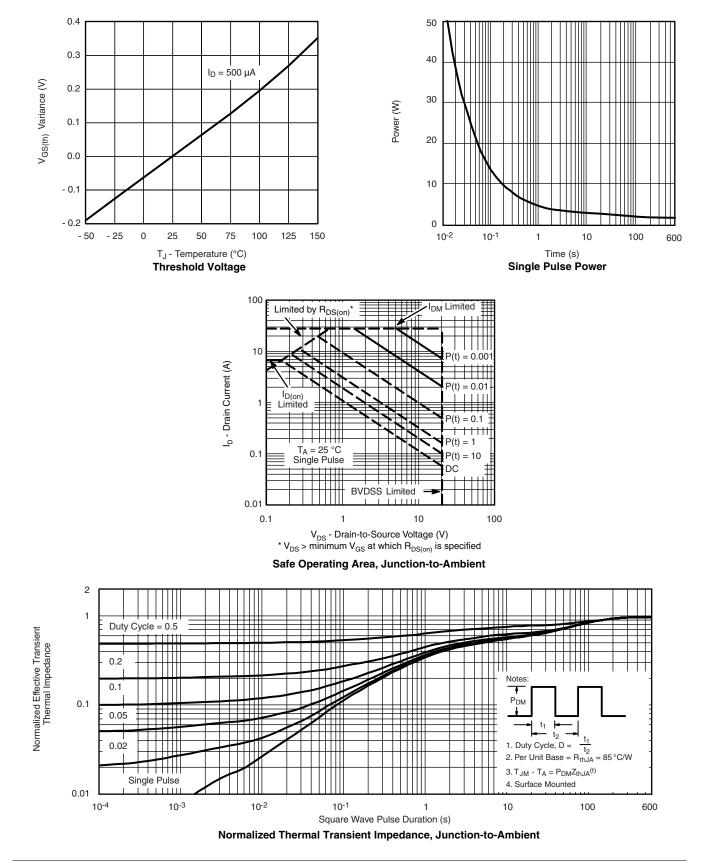
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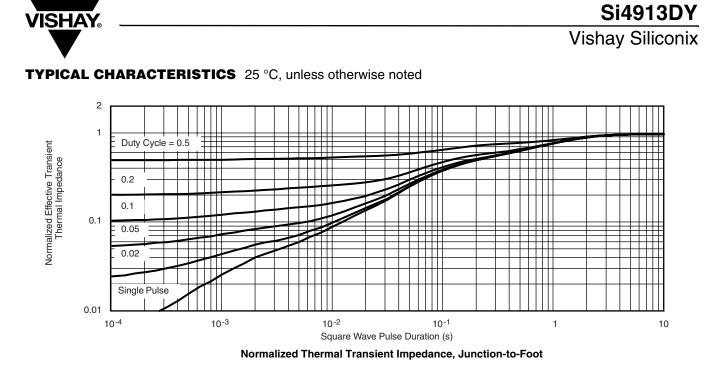
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Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg271997.



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