


<b>Notification Number:</b>	20230109000.0	<b>Notification Date:</b>	January 12, 2023
<b>Title:</b>	Datasheet for DP83TC812x-Q1		
<b>Customer Contact:</b>	<a href="#">Notification Manager</a>	<b>Dept:</b>	Quality Services
<b>Change Type:</b> Electrical Specification			
<b>Description of Change:</b>			
<p>Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.</p>			
		<b>DP83TC812S-Q1, DP83TC812R-Q1</b> SNLS654B – APRIL 2021 – REVISED JANUARY 2023	
<b>Changes from Revision A (December 2021) to Revision B (January 2023)</b>			<b>Page</b>
<ul style="list-style-type: none"> <li>• Added 'Functional Safety-Capable' to Feature List..... 1</li> <li>• Table 6-1 Pin Functions: Changed TX_CLK description to include (50 ohm Driver) for MII transmit clock..... 4</li> <li>• Table 6-4 Pin States -TC10 SLEEP: Changed the PULL TYPE of Pin 16 CLKOUT from PD-&gt;none..... 4</li> <li>• Added line to CLKOUT/GPIO2's description about which registers to program to disable switching..... 4</li> <li>• Added line to INT pin description. Reg 12-13 is recommended to be read only when INT_N is LOW..... 4</li> <li>• MDC clock rate changed from 25MHz-&gt;20MHz in Serial Management Interface Section of Pin Function Table..... 4</li> <li>• Updated lozh to clarify mapping of Rx_Ctrl and Rx_ER pins..... 17</li> <li>• Removed Supply ramp delay offset: For all supplies..... 22</li> <li>• Power-Up Timing figure corrected..... 25</li> <li>• PHY Operation State Diagram figure updated..... 44</li> <li>• Added Auto-clear note to register 0x18B[6]..... 45</li> <li>• Added XI clock PPM Table..... 50</li> <li>• Added Auto-clear note to register 0x18B[6]..... 58</li> <li>• Register 0x63E, clarified Bit Description..... 62</li> <li>• Register 0x63D, clarified Bit Description..... 62</li> <li>• Register 0x63C, clarified Bit Description..... 62</li> <li>• Register 0x63B, clarified Bit Description..... 62</li> <li>• Register 0x63A, clarified Bit Description..... 62</li> <li>• Register 0x639, clarified Bit Description..... 62</li> <li>• Register 0x451, clarified Bit Descriptions ..... 62</li> <li>• Register 0x18B has been added..... 62</li> <li>• Register 0x12, Bit 15 has been removed..... 62</li> </ul>			
The datasheet number will be changing.			
Device Family	Change From:	Change To:	
DP83TC812x-Q1	SNLS654A	SNLS654B	
<p>These changes may be reviewed at the datasheet links provided.  <a href="http://www.ti.com/product/DP83TC812S-Q1">http://www.ti.com/product/DP83TC812S-Q1</a>  <b>Error! Bookmark not defined.</b></p>			
<b>Reason for Change:</b>			
To accurately reflect device characteristics.			
<b>Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):</b>			
No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.			
<b>Changes to product identification resulting from this notification:</b>			
None.			
<b>Product Affected:</b>			
DP83TC812RRHARQ1	DP83TC812RRHATQ1	DP83TC812SRHARQ1	DP83TC812SRHATQ1

For questions regarding this notice, e-mails can be sent to the contact shown below or your local Field Sales Representative.

<b>Location</b>	<b>E-Mail</b>
WW Change Management Team	<a href="mailto:PCN_ww_admin_team@list.ti.com">PCN_ww_admin_team@list.ti.com</a>

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