



Title of Change:	Marking update for A5191HRTPG HART modem in PLCC package
Effective date:	17 August 2016
Contact information:	Contact your local ON Semiconductor Sales Office or <Min.Kwak@onsemi.com>
Type of notification:	ON Semiconductor will consider this change accepted.
Change category:	<input type="checkbox"/> Wafer Fab Change <input checked="" type="checkbox"/> Assembly Change <input type="checkbox"/> Test Change <input type="checkbox"/> Other _____

Change Sub-Category(s): <input type="checkbox"/> Manufacturing Site Change/Addition <input type="checkbox"/> Material Change <input type="checkbox"/> Manufacturing Process Change <input type="checkbox"/> Product specific change	<input type="checkbox"/> Datasheet/Product Doc change <input checked="" type="checkbox"/> Shipping/Packaging/Marking <input type="checkbox"/> Other: _____
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Sites Affected: <input type="checkbox"/> All site(s) <input type="checkbox"/> not applicable <input type="checkbox"/> ON Semiconductor site(s) :	<input checked="" type="checkbox"/> External Foundry/Subcon site(s) Amkor Technology Philippines P1
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Description and Purpose:

The top mark label of the A5191HRT PLCC package-option was changed from, "XXXXYZZ A5191HRTPG" to "A5191HRTPG AWLYYWW". Pin 1 position stays the same. Please refer to the decoding for, "AWLYYWW" in Figure 3. Changes were made in compliance with the specification, 12MON00232D.

Electrical characteristics are NOT impacted.

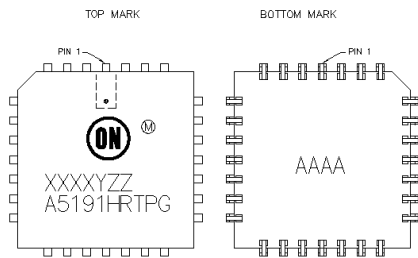


Figure 1. Previous marking.

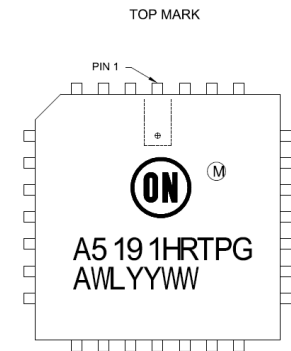


Figure 2. New Marking.

A = ASSEMBLY LOCATION
 WL = WAFER LOT
 YYWW = YEAR / WORK WEEK

Figure 3. Decoder.

List of affected Standard Parts:

- A5191HRTPG-XTD
- A5191HRTPG-XTP