

Notification Number:	20201006000	Notification Date:	Nov. 6, 2020
Title:	Datasheet for HD3SS3220		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:	Electrical Specification		

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



HD3SS3220

SLLSES1D – DECEMBER 2015 – REVISED SEPTEMBER 2020

Changes from Revision C (May 2017) to Revision D (September 2020)

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• Changed VDD to VDD5.....	3
• In Control Pins row of <i>Absolute Maximum Ratings</i> , DIR was in both VDD5 and VCC33. Removed DIR from VDD5.....	5
• Deleted C _(bus,I2C) from the <i>Recommended Operating Conditions</i> table	5
• From: When using 3.3 V for I ² C, customer must ensure VDD is above 3 V at all times. To: When using 3.3 V for I ² C, customer must ensure VDD5 is above 3 V at all times.....	6
• Changed the "I2C (SDA, SCL)" section of the <i>Timing Requirements</i> table.....	8
• Added t _{ENnCC_HI} parameter in section of the <i>Timing Requirements</i> table.....	8
• Added t _{VDD5V_PG} parameter in section of the <i>Timing Requirements</i> table.....	8
• Added note in section <i>DFP/Source – Downstream Facing Port</i> that ID pin will remain high until VBUS is at VSafe0V.....	14
• From: When a voltage level within the proper threshold is detected on CC1, the DIR pin is pulled low. To: When a voltage level within the proper threshold is detected on CC1, the DIR pin is high.....	15
• From: When a voltage level within the proper threshold is detected on CC2, the DIR pin is high. To: When a voltage level within the proper threshold is detected on CC2, the DIR pin is pulled low.....	15
• From: HD3SS3220 supports audio and debug accessories in UFP, DFP and DRP mode. To: HD3SS3220 supports audio and debug accessories in UFP, DFP and DRP mode by default.....	16
• Added note that UFP accessory support can be disabled by setting DISABLE_UFP_ACCESSORY register.....	16
• Added section on VDD5 and VCC33 power-on requirements.....	17
• Removed the Note about non-failsafe pins from <i>Dead Battery</i> section as this information is in the <i>VDD5 and VCC33 Power-On Requirements</i> section.....	19

The datasheet number will be changing.

Device Family	Change From:	Change To
HD3SS3220	SLLSES1C	SLLSES1D

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/HD3SS3220>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this notification:

None.

Product Affected:

HD3SS3220IRNHR	HD3SS3220IRNHT	HD3SS3220RNHR	HD3SS3220RNHT
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For questions regarding this notice, e-mails can be sent to the contacts shown below or your local Field Sales Representative.

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