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New Japan Radio Co.,Ltd.

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DUAL OPERATIONAL TRANSCONDUCTANCE AMPLIFIER

■ GENERAL DESCRIPTION

The NJM13600/13700 consist of two current controlled trans conductance amplifiers each with differential inputs and a push pull output. The two amplifiers share common supplies but otherwise operate independently. Linearizing diodes are provided at the inputs to reduce distortion and allow higher input levels. The result is a 10 dB signal-to-noise improvement referenced to 0.5 percent THD.Controlled impedance buffers are provided which are especially designed to complement the dynamic range of the amplifiers.

■ FEATURES

● Package Outline   DIP16,DMP16
● Bipolar Technology
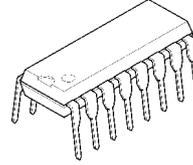
■ PACKAGE OUTLINE

NJM13600D
NJM13700D

NJM13600M
NJM13700M

■ PIN CONFIGURATION
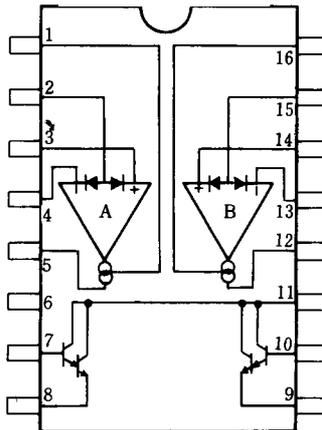

NJM13600D,NJM13600M
NJM13700D,NJM13700M

PIN FUNCTION

1.	AMP BIAS INPUT A	9. BUFFER OUTPUT B
2.	DIODE BIAS A	10. BUFFER INPUT B
3.	+INPUT	11. V^+
4.	−INPUT	12. OUTPUT B
5.	OUTPUT A	13. -INPUT B
6.	V^-	14. +INPUT B
7.	BUFFER INPUT A	15. DIODE BIAS B
8.	BUFFER OUTPUT A	16. AMP BIAS INPUT B

■ EQUIVALENT CIRCUIT

note: A dotted line is only 13600.

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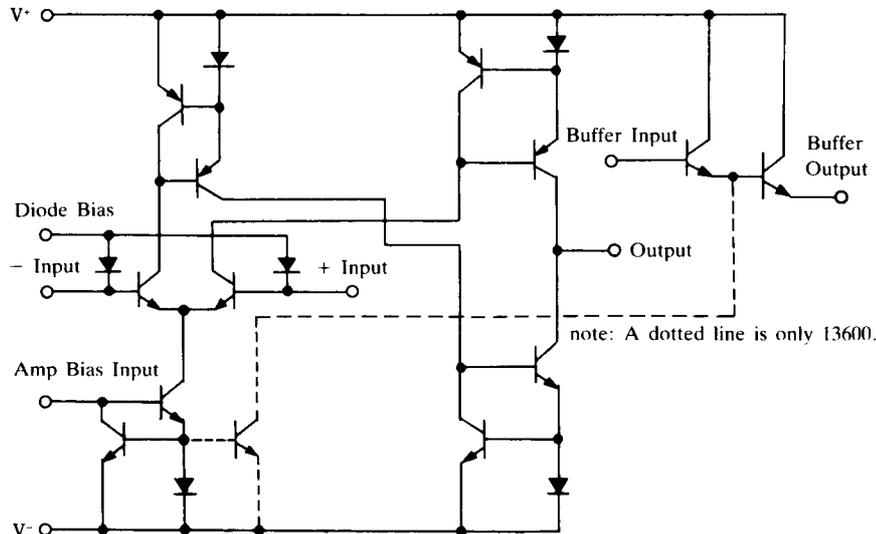
■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V^+/V^-	36 or ±18	V
Differential Input Voltage	V_{ID}	± 5	V
Diode Bias Current	I_D	2	mA
Amp Bias Current	I_{ABC}	2	mA
Buffer Output Current	I_O	20	mA
Power Dissipation	P_D	(DIP16) 570 (DMP16) 700 (note)	mW
DC Input Voltage	V_{IN}	V^+~V^-	V
Operating Temperature Range	T_{opr}	-40~+85	°C
Storage Temperature Range	T_{stg}	-40~+125	°C

(note) At on a ceramic PCB (10x20x0.635 mm)

■ ELECTRICAL CHARACTERISTICS

(V^+/V^-=±15V,Ta=25°C,I_{ABC}=500µA)

PARAMETER	SYMBOL	TEST CONDITION	13600 MIN.	13600 TYP.	13600 MAX.	13700 MIN.	13700 TYP.	13700 MAX.	UNIT
Input Offset Voltage (V_{OS})	V_{IO}		-	0.4	5	-	0.4	4	mV
Input Offset Voltage		I_{ABC}=5µA	-	0.3	5	-	0.3	4	mV
V_{OS} Including Diodes		Diode Bias Current,I_D=500µA	-	0.5	5	-	0.5	5	mV
Input Offset Change		5µA≦I_{ABC}≦500µA	-	0.1	-	-	0.1	3	mV
Input Bias Current	I_B		-	0.4	5	-	0.4	5	µA
Input Bias Current		(-20~+75°C)	-	1	8	-	1	8	µA
Forward Transconductance	gm		6700	9600	13000	6700	9600	13000	µS
		(-20~+75°C)	5400	-	-	5400	-	-	µS
gm Tracking		R_L=0,I_{ABC}=5µA	-	0.3	-	-	0.3	-	dB
Peak Output Current	I_{OP}	R_L=0,I_{ABC}=5µA	-	5	-	-	0	-	µA
Peak Output Current		R_L=0,I_{ABC}=500µA	350	500	650	350	500	650	µA
Peak Output Current		R_L=0 (-20~+75°C)	300	-	-	300	-	-	µA
Peak Output Voltage Positive	V_{OP}	R_L=∞,5µA≦I_{ABC}≦500µA	+12	+14.2	-	+12	+14.2	-	V
Peak Output Voltage Negative		R_L=∞,5µA≦I_{ABC}≦500µA	-12	-14.4	-	-12	-14.4	-	V
Operating Current	I_{CC}	I_{ABC}=500µA,two circuit	-	2.6	-	-	2.6	-	mA
V_{OS} Sensitivity Positive	SVR	$\Delta V_{OS}/\Delta V^+$	76.5	94	-	76.5	94	-	dB
V_{OS} Sensitivity Negative		$\Delta V_{OS}/\Delta V^-$	76.5	94	-	76.5	94	-	dB
Input Offset Current	I_{IO}		-	0.1	0.6	-	0.1	0.6	µA
CMRR	CMR		80	110	-	80	110	-	dB
Common Mode Range	V_{ICM}		± 12	± 13.5	-	± 12	± 13.5	-	V
Cross Talk	CT	20Hz<f<20kHz (note2)	-	-100	-	-	-100	-	dB
Differential Input Current	I_{ID}	I_{ABC}=0,Input=±4V	-	0.02	100	-	0.02	100	nA
Leakage Current	I_{LEAK}	I_{ABC}=0(Refer to Test Circuit)	-	0.2	100	-	0.2	100	nA
Input Resistance	R_{IN}		10	26	-	10	26	-	kΩ
Open Loop Bandwidth			-	2	-	-	2	-	MHz
Slew Rate	SR		-	50	-	-	50	-	V/µs
Buffer Input Current		(note2)	-	0.4	5	-	0.4	5	µA
Peak Buffer Output Voltage		(note2)	10	-	-	10	-	-	V

(note1) Open unless otherwise specified. The inputs to the buffers are grounded and outputs are open.

(note2) R_{OUT}=5kΩ connected from the buffer output to V^- and the input of buffer is connected to the transconductance amplifier output.

I_{ABC}=500µA

■ **TEST CIRCUIT**
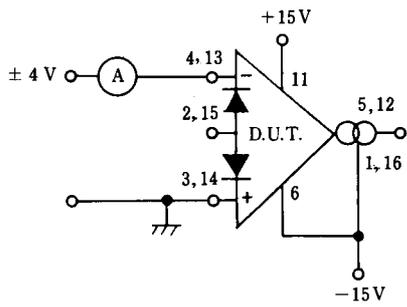

Differential Input Current
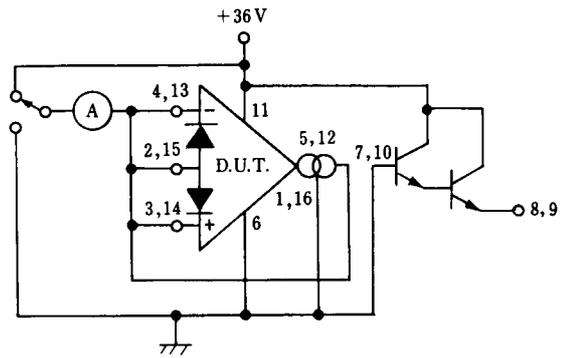
Leakage Current

■ **TYPICAL APPLICATIONS**

Unity Gain-Follower
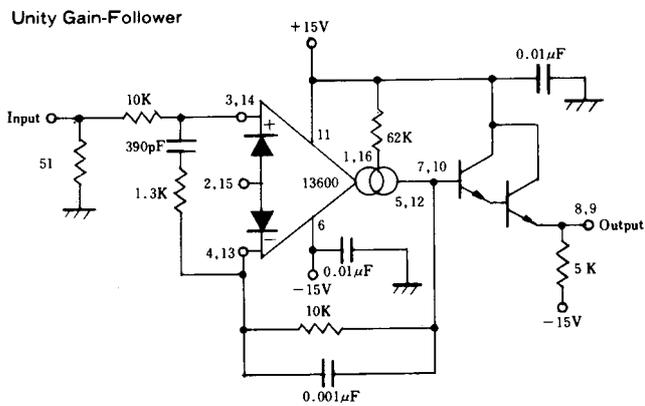
NJM13600/13700

■ TYPICAL CHARACTERISTICS

Distortion
vs. Differential Input Voltage
($V^+/V^- = \pm 15V$, $R_L = 10k\Omega$, $I_{ABC} = 1mA$, $Ta = 25°C$)

Distortion (%)

Differential Input Voltage V_{ID} (mV_{PP})

Not Using Diodes

Using Diodes
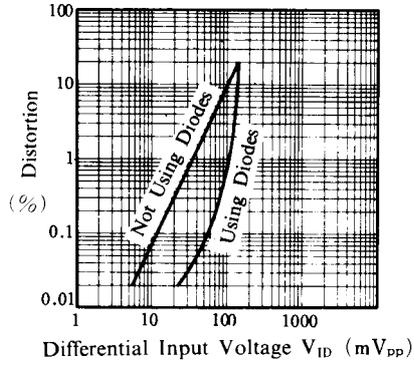
Voltage
vs. Amplifier Bias Current
($V^+/V^- = \pm 15V$, $R_L = 10k\Omega$, $Ta = 25°C$)

Output Voltage Relative V_O (dB)

Amplifier Bias Current I_{ABC} (μA)

$V_{IN} = 80mV_{P-P}$

$V_{IN} = 40mV_{P-P}$

Output Noise (BW = 20kHz)
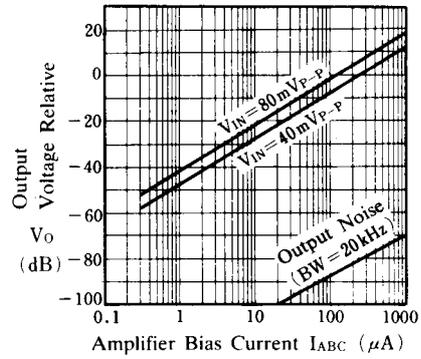
Output Noise Current
vs. Frequency
($Ta = 25°C$)

Output Noise Current I_{NO} ($pA/\sqrt{Hz}$)

Frequency f (Hz)

$I_{ABC} = 1mA$

$I_{ABC} = 100\mu A$

■ TYPICAL CHARACTERISTICS

Input Offset Voltage
(V⁺/V⁻ = ±15V, Ta = 25℃)

Input Offset Current
(V⁺/V⁻ = ±15V, Ta = 25℃)

Input Bias Current
(V⁺/V⁻ = ±15V, Ta = 25℃)

Peak Output Current
(V⁺/V⁻ = ±15V, Ta = 25℃)

Peak Output Voltage and Common Mode Range
(V⁺/V⁻ = ±15V, R_L = ∞, Ta = 25℃)

Leakage Current vs. Temperature
(+V_IN = −V_IN = V_O)
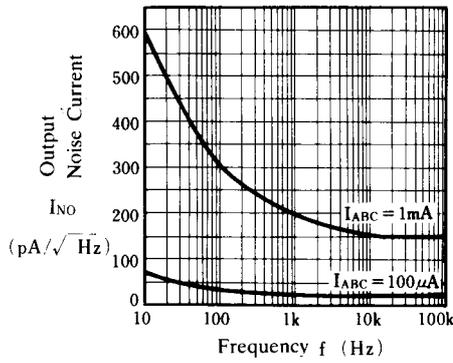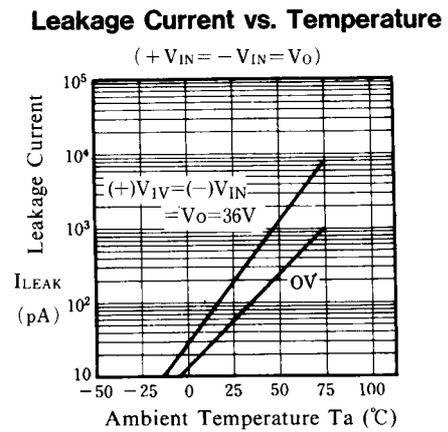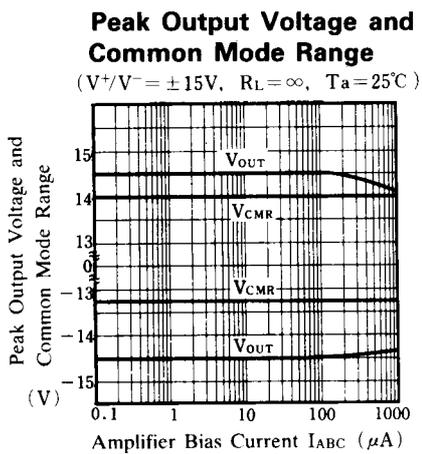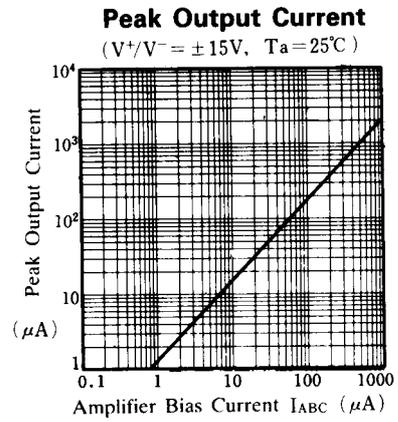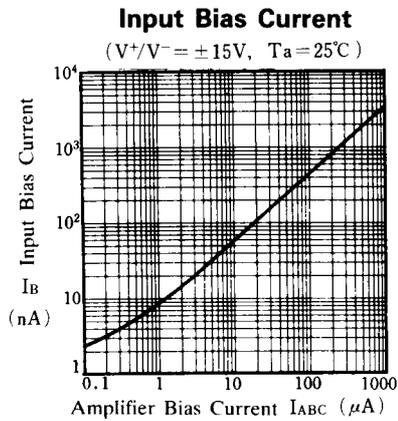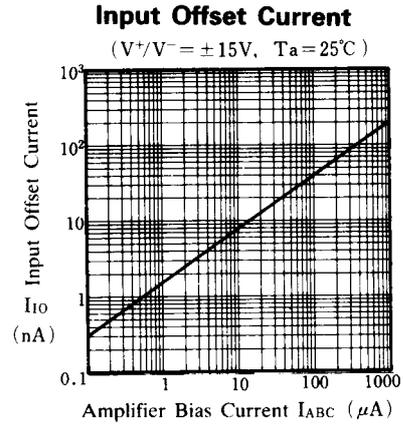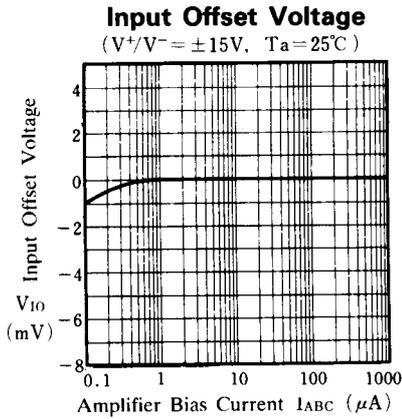
NJM13600/13700

## ■ TYPICAL CHARACTERISTICS

Input Leakage Current
(Ta=25℃)

Input Leakage Current I_{LEAK} (pA) vs. Input Differential Voltage V_{ID} (V)

Transconductance
(V⁺/V⁻=±15V, Pin 2.15 Open, Ta=25℃)

Transconductance g_m ($\mu\Omega$) vs. Amplifier Bias Current I_{ABC} (μA)
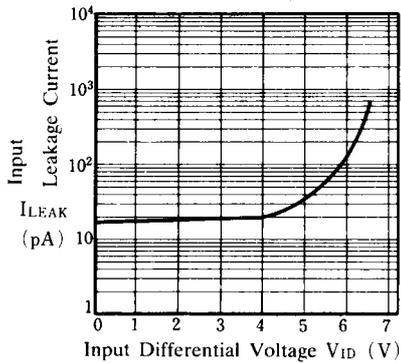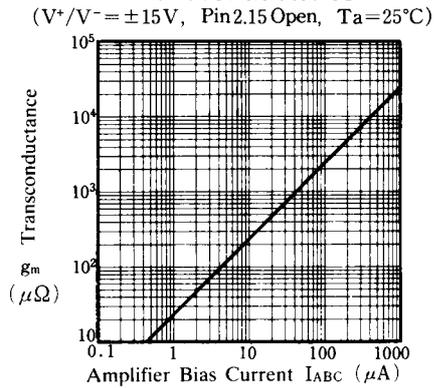
Input Resistance
(Pin 2.15 Open, Ta=25℃)

Input Resistance R_{IN} (MΩ) vs. Amplifier Bias Current I_{ABC} (μA)
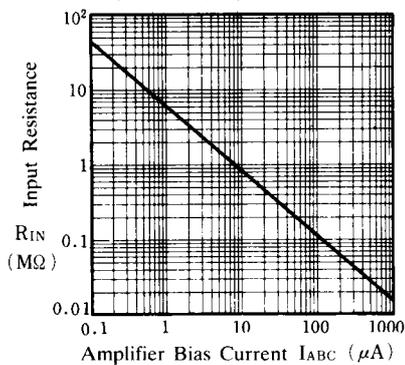
Amplifier Bias Voltage vs. Amplifier Bias Current
(Ta=25℃)

Amplifier Bias Voltage V_{ABC} (mV) vs. Amplifier Bias Current I_{ABC} (μA)
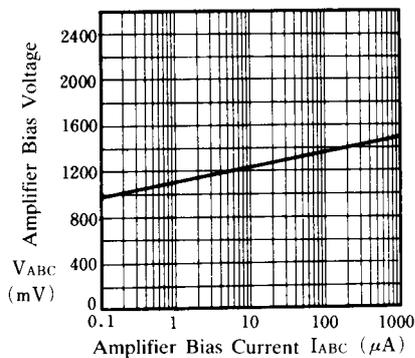
Input and Output Capacitance
(V⁺/V⁻=±15V, Ta=25℃)

Capacitance (pF) vs. Amplifier Bias Current I_{ABC} (μA)

C_{In}
C_{out}

Output Resistance
(Ta=25℃)

Output Resistance R_{OUT} (MΩ) vs. Amplifier Bias Current I_{ABC} (μA)