

<b>PCN Number:</b>	20201217001.2B	<b>PCN Date:</b>	Mar 18, 2021		
<b>Title:</b>	Qualification of RFAB as an additional Fab site option for select LBC7 devices				
<b>Customer Contact:</b>	<a href="#">PCN Manager</a>	<b>Dept:</b>	Quality Services		
<b>Proposed 1<sup>st</sup> Ship Date:</b>	Jul 25, 2021	<b>Estimated Sample Availability:</b>	Date provided at sample request.		
<b>Change Type:</b>					
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Assembly Process		
<input type="checkbox"/>	Design	<input type="checkbox"/>	Assembly Materials		
<input type="checkbox"/>	Test Site	<input type="checkbox"/>	Electrical Specification		
<input type="checkbox"/>	Wafer Bump Site	<input type="checkbox"/>	Mechanical Specification		
<input checked="" type="checkbox"/>	Wafer Fab Site	<input checked="" type="checkbox"/>	Packing/Shipping/Labeling		
		<input type="checkbox"/>	Test Process		
		<input type="checkbox"/>	Wafer Bump Material		
		<input checked="" type="checkbox"/>	Wafer Bump Process		
		<input type="checkbox"/>	Wafer Fab Materials		
		<input checked="" type="checkbox"/>	Wafer Fab Process		
		<input type="checkbox"/>	Part number change		
<b>PCN Details</b>					
<b>Description of Change:</b>					
<p><b>PCN Revision B is a correction to the Description of Change section as shown below. Devices highlighted in bold and yellow highlight will have the addition of a PI Layer as part of this change.</b></p> <p>Texas Instruments is pleased to announce the qualification of its RFAB fabrication facility as an additional Wafer Fab source for the selected devices listed in "Product Affected" section.</p>					
<b>Current Site</b>			<b>Additional Site</b>		
<b>Current Fab Site</b>	<b>Process</b>	<b>Wafer Diameter</b>	<b>Additional Fab Site</b>	<b>Process</b>	<b>Wafer Diameter</b>
FFAB	LBC7	200mm	RFAB	LBC7	300mm
<b>Current Top Layer Material</b>			<b>New Top Layer Material</b>		
BOAC (No PI)			BOAC + PI		
Qual details are provided in the Qual Data Section.					
<b>Reason for Change:</b>					
Continuity of Supply					
<b>Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):</b>					
None					
<b>Changes to product identification resulting from this PCN:</b>					
<b>Current</b>					
Chip Site	Chip Site Origin (20L)	Chip Site Country Code (21L)	Chip Site City		
FR-BIP-1	TID	DEU	Freising		
<b>New Fab Site</b>					
Chip Site	Chip Site Origin (20L)	Chip Site Country Code (21L)	Chip Site City		
<b>RFAB</b>	<b>RFB</b>	<b>USA</b>	<b>Richardson</b>		

Sample product shipping label (not actual product label)



MADE IN: Malaysia  
2DC: 20:



(1P) SN74LS07NSR  
(Q) 2000 (D) 0336  
(31T) LOT: 3959047MLA  
(4W) TKY (1T) 7523483SI2  
(P)  
(2P) REV: (V) 0053817  
(20L) CSO: SHE (21L) CCO: USA  
(22L) ASO: MLA (23L) ACO: MYS

MSL 2 / 260C / 1 YEAR SEAL DT  
MSL 1 / 235C / UNLIM 03/29/04

OPT:  
ITEM: 39  
LBL: 5A (L) TO: 1750

**Product Affected:**

UCC27524A1QDGNRQ1

**UCC27524AQDGNRQ1**

**UCC27524AQDRQ1**

**Automotive Qualification Summary**

(As per AEC-Q100 and JEDEC Guidelines)

**UCC27524A1QDGNRQ1 and UCC27524AQDRQ1 FFAB to RFAB**

Approve Date 22-October-2020

**Qualification Results**

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS/ Lot	Test Name / Condition	Duration	Qual Device: UCC27524A1QDGNRQ1	Qual Device: UCC27524AQDRQ1	Process QBS Reference TP52543QRTE	Product QBS Reference UCC27524AQDRQ1	Package QBS Reference PGA308AQDGSRQ1	Package QBS Reference UCC27524
<b>Test Group A – Accelerated Environment Stress Tests</b>												
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST, 130C/85%RH	96 Hours	-	-	3/231/0	1/77/0	3/231/0	3/231/0
AC	A3	JEDEC JESD22-A102	3	77	Autoclave 121C	96 Hours	-	-	3/231/0	1/77/0	3/231/0	-
uHAST	A3	JEDEC JESD22-A102	3	77	unBiased HAST, 130C/85%RH	96 Hours	-	1/77/0	-	-	-	3/231/0
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle, -65/150C	500 Cycles	-	1/77/0	3/231/0	1/77/0	3/231/0	3/231/0
PTC	A5	JEDEC JESD22-A105	1	45	Power Temperature Cycle	1000 Cycles	N/A	N/A	N/A	N/A	N/A	N/A
HTSL	A6	JEDEC JESD22-A103	1	45	High Temp. Storage Bake, 175C	500 Hours	-	1/45/0	1/45/0	1/45/0	1/45/0	3/231/0
<b>Test Group B – Accelerated Lifetime Simulation Tests</b>												
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test, 150C	408 Hours	-	-	3/231/0	-	3/231/0	-
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test, 140C	480 Hours	-	-	-	2/154/0	-	-
ELFR	B2	AEC Q100-008	3	800	Early Life Failure Rate, 125C	48 Hours	-	-	3/2640/0	1/800/0	3/2400/0	-
EDR	B3	AEC Q100-005	3	77	NVM Endurance, Data Retention, and Operational Life	--	N/A	N/A	N/A	N/A	3/231/0	-
<b>Test Group C – Package Assembly Integrity Tests</b>												
WBS	C1	AEC Q100-001	1	30	Bond Shear (Cpk>1.67)	Wires	1/30/0	1/30/0	-	-	-	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Bond Pull (Cpk>1.67)	Wires	1/30/0	1/30/0	-	-	-	3/90/0
SD	C3	JEDEC JESD22-B102	1	15	Solderability (>95% Lead Coverage)	Pb & Pb-Free	-	-	1/15/0	-	-	-
PD	C4	JEDEC JESD22-B100 and B108	3	10	Physical Dimensions (Cpk>1.67)	--	-	-	3/30/0	-	-	3/30/0
<b>Test Group D – Die Fabrication Reliability Tests</b>												
EM	D1	JESD61	-	-	Electromigration	--	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	--	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Injection Carrier	--	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
NBTI	D4	-	-	-	Negative Bias Temperature Instability	--	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	--	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
<b>Test Group E – Electrical Verification Tests</b>												
HBM	E2	AEC Q100-002	1	3	ESD - HBM	4000 V	1/3/0	1/3/0	-	-	-	-
CDM	E3	AEC Q100-011	1	3	ESD - CDM	1000 V	1/3/0	1/3/0	-	-	-	-
LU	E4	AEC Q100-004	1	6	Latch-up	(Per AEC Q100-004)	1/6/0	1/6/0	1/6/0	-	-	-
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Room, Hot, & Cold Test	3/90/0	3/90/0	3/90/0	3/90/0	-	-

Qual Device UCC27524A1QDGNRQ1 is qualified at LEVEL2-260C  
Qual Device UCC27524AQDRQ1 is qualified at LEVEL1-260C

A1 (PC): Preconditioning:

Performed for THB, Biased HAST, AC, uHAST, TC & PTC samples, as applicable.

Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40°C to +150°C  
Grade 1 (or Q): -40°C to +125°C  
Grade 2 (or T): -40°C to +105°C  
Grade 3 (or I): -40°C to +85°C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold: HTOL, ED  
Room/Hot: THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU  
Room: AC/uHAST

Green/Pb-free Status:

Qualified Pb-Free(SMT) and Green

For questions regarding this notice, e-mails can be sent to the contacts shown below or your local Field Sales Representative.

<b>Location</b>	<b>E-Mail</b>
USA	<a href="mailto:PCNAmericasContact@list.ti.com">PCNAmericasContact@list.ti.com</a>
Europe	<a href="mailto:PCNEuropeContact@list.ti.com">PCNEuropeContact@list.ti.com</a>
Asia Pacific	<a href="mailto:PCNAsiaContact@list.ti.com">PCNAsiaContact@list.ti.com</a>
WW PCN Team	<a href="mailto:PCN_ww_admin_team@list.ti.com">PCN_ww_admin_team@list.ti.com</a>

### **IMPORTANT NOTICE AND DISCLAIMER**

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES “AS IS” AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI’s products are provided subject to TI’s Terms of Sale ([www.ti.com/legal/termsofsale.html](http://www.ti.com/legal/termsofsale.html)) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI’s provision of these resources does not expand or otherwise alter TI’s applicable warranties or warranty disclaimers for TI products.