



January 12th, 2015

C8051F388-B Errata

Erratum #	Title	Impact	Affected Revision	Fixed Revision
1	External Oscillator on 32-pin packages	Major	Revision A	Not fixed
2	INT2 pin non-functional	Major	Revision A	Revision B

Impact definition: Each erratum is marked with an impact, as defined below:

- Minor – Workaround exists.
- Major – Errata that do not conform to the data sheet or standard.
- Information – The device behavior is not ideal but acceptable. Typically, the data sheet will be changed to match the device behavior.

1. **Description:** The external oscillator circuit is non-functional on the 32-pin packages in C, RC, and Crystal oscillator modes for revision A devices. The external CMOS clock modes are functional. The affected part numbers are as follows: C8051F389-GQ, C8051F389-GM, C8051F38B-GQ, C8051F38B-GM.

Impact: The external oscillator cannot be used to generate a clock when used in the C, RC or Crystal modes on the affected devices.

Workaround: One of the internal oscillators or an external CMOS clock may be used to provide a clock to the device and/or its peripherals.

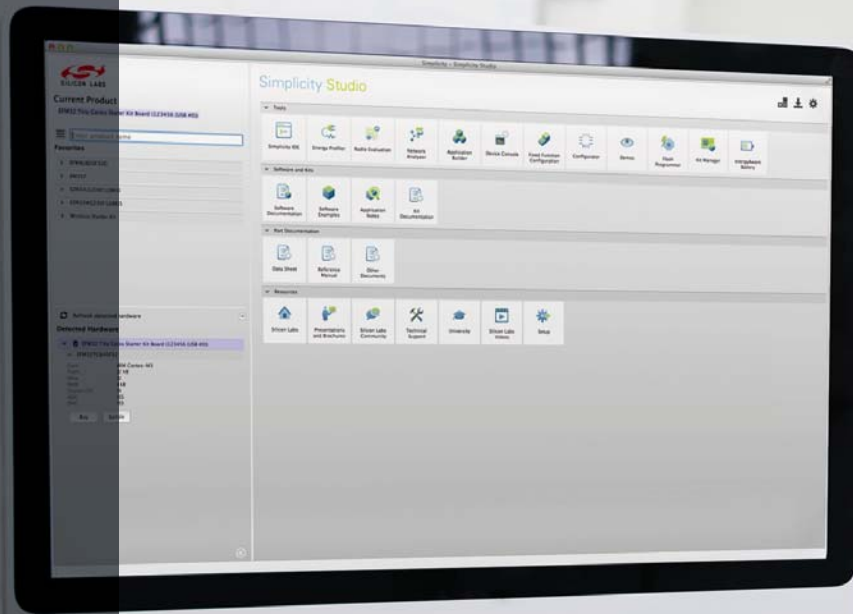
Resolution: Revision C of the device will allow the use of the external oscillator in C, RC and Crystal modes.

2. **Description:** The INT2 pin is non-functional on revision A C8051F388/9/A/B devices.

Impact: The INT2 pin cannot be used to generate an interrupt.

Workaround: The /INT0 and /INT1 external interrupts are available on P0 and can still be used to generate an external pin interrupt.

Resolution: Revision B of the device will allow the use of the INT2 pin.



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