

## Product brief

# EiceDRIVER™ 2EDi product family

Fast, robust, dual-channel galvanic isolated gate driver ICs

### Galvanic isolated EiceDRIVER™ family overview

The 2EDi is a family of dual-channel isolated gate driver ICs designed to drive Si MOSFETs, SiC MOSFETs and GaN power switches.

Isolation is achieved by means of Infineon coreless transformer (CT) technology which guarantees robust operation and industry benchmark common-mode rejection (CMTI).

The high propagation delay accuracy and low channel-to-channel mismatch makes the product ideal for use in fast-switching power system. In addition, high CMTI, high reverse current capability and fast clamping of the output below UVLO guarantees reliable operation in the application.

### EiceDRIVER™ 2EDi generation II: what is new?

- › DSO-14 packages with increased channel-to-channel creepage to ease the layout or enable the use with higher working voltages or worst pollution layout
- › New LGA 4 mm x 4 mm enabling 70 percent space saving
- › Fulfills the latest component standard for magnetic couplers (IEC 60747-17)
- › Programmable dead-time and shoot-through functionality

### Applications

- › Server
- › Telecom
- › DC-DC converters
- › Power tools
- › Industrial SMPS
- › Low-speed electrical vehicles
- › Solar micro inverter
- › LED Lighting



Product key features	Product benefits	System benefits
<b>Fast power switching with accurate timing</b> <ul style="list-style-type: none"> <li>› 5 A/9 A source/sink currents</li> <li>› Propagation delay typ. 38 ns with 6 ns channel-to-channel precision</li> </ul>	<b>Efficiency gain and lower losses</b> <ul style="list-style-type: none"> <li>› Strong driving enables reduced switching losses</li> <li>› Accurate timing enables dead-time optimization or synchronized driving in case of parallel MOSFETs</li> </ul>	<b>Enabling higher system efficiency and higher power density designs</b>
<b>Optimized for area and system BOM</b> <ul style="list-style-type: none"> <li>› &lt; 1 Ω source and sink output resistance</li> <li>› Output stages with 5 A reverse current capability</li> </ul>	<b>Improved thermal behavior at smaller form factor</b> <ul style="list-style-type: none"> <li>› Most of the driving power is dissipated externally with reduced thermal stress on the driver</li> <li>› Eliminates two costly protection diodes on the gate driver outputs</li> </ul>	<b>Improving long term competitive cost position, integration and mass manufacturability</b>
<b>Robust</b> <ul style="list-style-type: none"> <li>› CMTI &gt;150 V/ns</li> <li>› Under voltage lockout function for switch protection</li> <li>› Dead time control and shoot through protection</li> </ul>	<b>Protection and safe operation</b> <ul style="list-style-type: none"> <li>› Reliable driver operation against fast switching transients</li> <li>› MOSFET is protected from thermal stress in abnormal conditions</li> <li>› Protection against noise and spurious pulses</li> </ul>	<b>Extending end-product lifetime by improving safe operation of power switches in normal and abnormal field (grid) conditions</b>
<b>Output-to-output channel isolation</b> <ul style="list-style-type: none"> <li>› Functional level galvanic isolation</li> </ul>	<b>Flexible configurations</b> <ul style="list-style-type: none"> <li>› High-side + low-side, high-side + high-side, low-side + low-side</li> </ul>	<b>Lower EMI by ground isolation</b> Robust Level shifting
<b>Input-to-output channel isolation</b> <ul style="list-style-type: none"> <li>› Functional, basic and reinforced galvanic isolation</li> </ul>	<b>Regulatory safety</b> <ul style="list-style-type: none"> <li>› Functional for level-shifting and ground bounce immunity</li> <li>› Reinforced for control of primary-side MOSFETs from secondary-side controller</li> </ul>	<b>Simplified safety approval through component standards (UL1577, IEC60747-17) and system standards (IEC62368) certificates</b>



# 2EDi EiceDRIVER™ family

Fast, robust, dual-channel galvanic isolated gate driver ICs

## Product portfolio

Product	Orderable number (OPN) part	Package			Galvanic isolation		Output stages			Control inputs	
		Type	Pins	Ch-Ch mm	Certification	CMTI	Source/sink currents <sup>3)</sup>	UVLO on nom.	UVLO start-up	STP / DTC option	DIS / EN
2EDS7165H	2EDS7165HXUMA1	300mil DSO	16 pin	2.5	$V_{ISO} 5.7 \text{ kV}_{rms}$ UL 1577 Reinforced EN 62368-1	150 V/ns	1 A/2 A	4 V	5 $\mu$ s	-	DIS
2EDS8165H	2EDS8165HXUMA2	300mil DSO	16 pin	2.5		150 V/ns	1 A/2 A	8 V	5 $\mu$ s	-	DIS
2EDS8265H	2EDS8265HXUMA2	300mil DSO	16 pin	2.5		150 V/ns	5 A/9 A	8 V	5 $\mu$ s	-	DIS
2EDS9265H	2EDS9265HXUMA1	300mil DSO	16 pin	2.5	$V_{ISO} 5.7 \text{ kV}_{rms}$ UL 1577 Reinforced EN 62368-1	150 V/ns	5 A/9 A	13 V	5 $\mu$ s	-	DIS
2EDR8259H	2EDR8259HXUMA1 <sup>4)</sup>	300mil DSO	16 pin	2.5		150 V/ns	5 A/9 A	8 V	2.5 $\mu$ s	yes	DIS
2EDR7259X	2EDR7259XXUMA1 <sup>4)</sup>	300mil DSO	14 pin	3.3		$V_{IOTM} = 8 \text{ kV}_{pk}$ VDE 0884-11 $V_{ISO} 5.7 \text{ kV}_{rms}$ UL 1577 Reinforced EN 62368-1	150 V/ns	5 A/9 A	4 V	2.5 $\mu$ s	yes
2EDR8259X	2EDR8259XXUMA1 <sup>4)</sup>	300mil DSO	14 pin	3.3	150 V/ns		5 A/9 A	8 V	2.5 $\mu$ s	yes	DIS
2EDR9259X	2EDR9259XXUMA1 <sup>4)</sup>	300mil DSO	14 pin	3.3	150 V/ns		5 A/9 A	15 V	2.5 $\mu$ s	yes	DIS
2EDR8258X	2EDR8258XXUMA1 <sup>4)</sup>	300mil DSO	14 pin	3.3	$V_{IOTM} = 8 \text{ kV}_{pk}$ VDE 0884-11 <sup>1)</sup> $V_{ISO} 5.7 \text{ kV}_{rms}$ UL 1577 <sup>1)</sup> Reinforced EN 62368-1 <sup>1)</sup>	150 V/ns	5 A/9 A	8 V	2.5 $\mu$ s	yes	EN
2EDR6258X	2EDR6258XXUMA1 <sup>4)</sup>	300mil DSO	14 pin	3.3		150 V/ns	5 A/9 A	12 V	2.5 $\mu$ s	yes	EN
2EDR9258X	2EDR9258XXUMA1 <sup>4)</sup>	300mil DSO	14 pin	3.3		150 V/ns	5 A/9 A	15 V	2.5 $\mu$ s	yes	EN
2EDF7175F	2EDF7175FXUMA2	150mil DSO	16 pin	2.5	1.5 kV <sub>DC</sub> functional	150 V/ns	1 A/2 A	4 V	5 $\mu$ s	-	DIS
2EDF7275F	2EDF7275FXUMA2	150mil DSO	16 pin	2.5	1.5 kV <sub>DC</sub> functional	150 V/ns	5 A/9 A	4 V	5 $\mu$ s	-	DIS
2EDF8275F	2EDF8275FXUMA1	150mil DSO	16 pin	2.5	1.5 kV <sub>DC</sub> functional	150 V/ns	5 A/9 A	8 V	5 $\mu$ s	-	DIS
2EDF9275F	2EDF9275FXUMA1	150mil DSO	16 pin	2.5	1.5 kV <sub>DC</sub> functional	150 V/ns	5 A/9 A	13 V	5 $\mu$ s	-	DIS
2EDB8259F	2EDB8259FXUMA1 <sup>4)</sup>	150mil DSO	16 pin	3.3	$V_{ISO} = 3 \text{ kV}_{rms}$ UL 1577 <sup>2)</sup>	150 V/ns	5 A/9 A	8 V	2.5 $\mu$ s	yes	DIS
2EDB7259Y	2EDB7259YXUMA1 <sup>4)</sup>	150mil DSO	14 pin	3.3	$V_{ISO} = 3 \text{ kV}_{rms}$ UL 1577 <sup>2)</sup>	150 V/ns	5 A/9 A	4 V	2.5 $\mu$ s	yes	DIS
2EDB8259Y	2EDB8259YXUMA1 <sup>4)</sup>	150mil DSO	14 pin	3.3	$V_{ISO} = 3 \text{ kV}_{rms}$ UL 1577 <sup>2)</sup>	150 V/ns	5 A/9 A	8 V	2.5 $\mu$ s	yes	DIS
2EDB9259Y	2EDB9259YXUMA1 <sup>4)</sup>	150mil DSO	14 pin	3.3	$V_{ISO} = 3 \text{ kV}_{rms}$ UL 1577 <sup>2)</sup>	150 V/ns	5 A/9 A	15 V	2.5 $\mu$ s	yes	DIS
2EDF7275K	2EDF7275KXUMA2	5x5 LGA	13 pin	1	1.5 kV <sub>DC</sub> functional	150 V/ns	5 A/9 A	4 V	5 $\mu$ s	-	DIS
2EDB7259K	2EDB7259KXUMA1 <sup>4)</sup>	5x5 LGA	13 pin	1	$V_{ISO} = 2.5 \text{ kV}_{rms}$ UL 1577 <sup>2)</sup>	150 V/ns	5 A/9 A	4 V	2.5 $\mu$ s	yes	DIS
2EDB8259K	2EDB8259KXUMA1 <sup>4)</sup>	5x5 LGA	13 pin	1	$V_{ISO} = 2.5 \text{ kV}_{rms}$ UL 1577 <sup>2)</sup>	150 V/ns	5 A/9 A	8 V	2.5 $\mu$ s	yes	DIS
2EDB7259E	2EDB7259EXUMA1 <sup>4)</sup>	4x4 LGA	13 pin	0.75	$V_{ISO} = 2.25 \text{ kV}_{rms}$ UL 1577 <sup>2)</sup>	150 V/ns	5 A/9 A	4 V	2.5 $\mu$ s	yes	DIS
2EDB8259E	2EDB8259EXUMA1 <sup>4)</sup>	4x4 LGA	13 pin	0.75	$V_{ISO} = 2.25 \text{ kV}_{rms}$ UL 1577 <sup>2)</sup>	150 V/ns	5 A/9 A	8 V	2.5 $\mu$ s	yes	DIS

<sup>1)</sup>Planned <sup>2)</sup>Pending <sup>3)</sup>typ.@ 12 V to 18 V VDD <sup>4)</sup>2EDi gen. II coming soon

[www.infineon.com/2EDi](http://www.infineon.com/2EDi)

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