

# Product / Process Change Notification



N° 2014-015-A

Dear Customer,

Please find attached our INFINEON Technologies PCN:

## ISO1H81xG – Design Change

Important information for your attention:

- Please respond to this PCN by indicating your decision on the approval form, sign it and return to your sales partner before 30. June 2014.
- Infineon aligns with the widely-recognized JEDEC STANDARD "JESD46", which stipulates: "Lack of acknowledgement of the PCN within 30 days constitutes acceptance of the change."

Your prompt reply will help Infineon Technologies to assure a smooth and well executed transition. If Infineon does not hear from your side by the due date, we will assume your full acceptance to this proposed change and its implementation.

Your attention and response to this matter is greatly appreciated.

Disclaimer:

If we do not receive any response within the given time limit we consider this as the acceptance of the PCN.

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**SUBJECT OF CHANGE:** Improvement of the system-level robustness

<b>PRODUCTS AFFECTED:</b>	<b>SalesName</b>	<b>SP</b>	<b>OPN</b>	<b>Package</b>
	ISO1H811G	SP000413798	ISO1H811GAUMA1	PG-DSO-36
	ISO1H812G	SP000413800	ISO1H812GAUMA1	PG-DSO-36
	ISO1H815G	SP000555576	ISO1H815GAUMA1	PG-DSO-36
	ISO1H816G	SP000555578	ISO1H816GAUMA1	PG-DSO-36

**REASON OF CHANGE:** Improved turn - off of outputs in case of rapid power supply voltage decrease.

<b>DESCRIPTION OF CHANGE:</b>	<b><u>OLD</u></b>	<b><u>NEW</u></b>
	■ Standard supply voltage sensitivity	Improved supply voltage sensitivity

**PRODUCT IDENTIFICATION:** Traceability is ensured by datecode.

## TIME SCHEDULE:

■ Final qualification report:	October 2014
■ First samples available:	July 2014
■ Start of delivery:	From January 2015 onwards

**ASSESSMENT:** No change in form, fit and functionality

<b>DOCUMENTATION:</b>	2_cip_14015_qualification plan
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**PCN 2014-015-A  
ISO1H81xG – Design Change**



**Final Qualification Report**

**Date: 2014-11-18**

**Reason for choosing following test vehicle:** ISO1H811G, same size, technology, package, locations  
**Extension of qualification:** ISO1H812G, ISO1H815G, ISO1H816G  
**Assessment of Q-Results** Pass

Reference Products				ISO1H811G
Test description	Abbr.	Condition	Readout	
<b>Pre-Conditioning</b> J-STD-20-D JEESD22 A111	<b>PC</b>			-
<b>Temperature Cycling</b> JESD22 A104	<b>TC*</b>	Ta min = -55 °C Ta max = +150 °C	0 cyc precon 500 cyc 1000 cyc	-
<b>Autoclave</b> JESD22 A102	<b>AC*</b>	Ta = 121°C RH = 100%	0 h precon 96 h	-
<b>Temperature Humidity Bias</b> JESD22 A101	<b>THB*</b>	T = 85 °C RH = 85%	0 h precon 168 h 500 h 1000 h	-
<b>High Temperature Storage Life</b> JESD22 A-103	<b>HTSL</b>	Ta =150°C	0 h precon 168 h 500 h 1000 h	-
<b>High Temperature Operating Life</b> JESD22 A108	<b>HTOL</b>	Tj = 150 °C	0 h precon 168 h 500 h 1000 h	0 / 77 - 0 / 77 0 / 77 0 / 77
<b>ESD Characterization HBM</b> JEDEC22 A114	<b>ESD</b>	HBM		HBM/2 Withstand Voltage 2500V
<b>ESD Characterization CDM</b> JEDEC22 C101	<b>ESD</b>	CDM		CDM/Class 3 Withstand Voltage 1500V
<b>Latch-Up</b> JESD78	<b>LU</b>			Pass
<b>Electrical Distribution</b>	<b>ED</b>	-40 °C +25 °C +125 °C		-

\* PC is done only for SMD Packages before AC, TC, THB, HAST stress tests

Abbreviations - not performed